

PTO/SB/088 (08-03)

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|---|------|--------------------------|-------------------|
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary) | | Complete if Known | |
| | | Application Number | 10/644,684 |
| | | Filing Date | 08/19/2003 |
| | | First Named Inventor | Robert A. Dunstan |
| | | Art Unit | Not Yet Assigned |
| | | Examiner Name | Not Yet Assigned |
| Sheet 1 | of 1 | Attorney Docket Number | 110349-133959 |

| NON PATENT LITERATURE DOCUMENTS | | | |
|---------------------------------|-----------------------|---|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| JC | 1 | "Advanced Configuration and Power Interface Specification," Compaq Computer Corporation, et al., Revision 2.0c, October 30, 2003, pp. ii-562 | |
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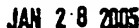
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| Examiner Signature | Jennifer Cho | Date Considered | 10/19/06 |
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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449.

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| Sheet | 1 | of | 2 |
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| Filing Date | 8/19/03 |
| First Named Inventor | Dunstan et al. |
| Art Unit | not yet known |
| Examiner Name | not yet known |
| Attorney Docket Number | 110349-133959 |

U. S. PATENT DOCUMENTS

| FOREIGN PATENT DOCUMENTS | | | | | | |
|--------------------------|-----------------------|---|------------------|---|---|----------------|
| Examiner Initials* | Cite No. ¹ | Foreign Patent Document | Publication Date | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear | T ^o |
| | | Country Code ² Number ⁴ Kind Code ³ (if known) | MM-DD-YYYY | | | |
| JC | 3 | EP 0 805 386 A1 | 11/5/97 | IBM | | |
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| Examiner Signature | <i>Jennifer Cho</i> | Date Considered | <i>10/19/06</i> |
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if KnownSheet 2

of 2

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| Art Unit | not yet known |
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NON PATENT LITERATURE DOCUMENTS

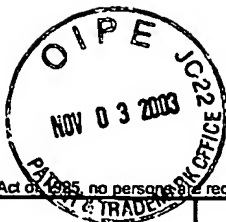
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
|-----------------------|--------------------------|---|----------------|
| JC | 4 | Advanced Configuration and Power Interface Specification, Revision 2.0, 7/27/00, Compaq Computer Corporation et al. | |
| JC | 5 | Advanced Power Management, BIOS Interface Specification, Revision 1.2, 2/96, Intel Corporation and Microsoft Corporation | |
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Examiner
Signature*Jennifer Cho*Date
Considered*10/19/06*

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Sheet 1 of 1

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| JC | 1 | "Advanced Configuration and Power Interface Specification," Compaq Computer Corporation, et al., Revision 2.0c, October 30, 2003, pp.26-44 | |
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